Subject: New Munin schematic
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Date: 12/4/2013 10:52 AM
To: "Dick Faust" <k9ivb@cox.net>

Hi Dick.

I attach an updated schematic for Munin2 dated 041213. The only difference from the old schematic is the inclusion of 4 extra components for better Bias stabilisation when used on 24V Vdd.

There is no space on the PCB for them so you have to use
leaded components and solder them directly.

The extra components are: D7, D8, R54 and R56. R54 is a small 10K potmeter and R56 is a 10K NTC resistor of the drop type. It must be placed in thermal contact with one of the output transistors. I solder it to one of the source leads with short wire and the body of the NTC touching the transistor. Use heat paste between the two.

Try to start the bias current adjustment with R54 set to 3,5 KOhm. Adjust R39 and R40 for 1A bias in each of the PA transistors. Wait while the transistors heat up and see if the current increase or decrease. If it increase, adjust R45 for a lower value until the current stops increasing. It can take some back and forth adjustments to get the bias current stable after warm up.

It can of course be used also on 13,8V Vdd.

73, Kjell
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Sendt med Operas e-postklient: http://www.opera.com/mail/

Attachments:

Munin2_041213.pdf	29.3 KB